

Deadlock and lock freedom in the linear π -calculus

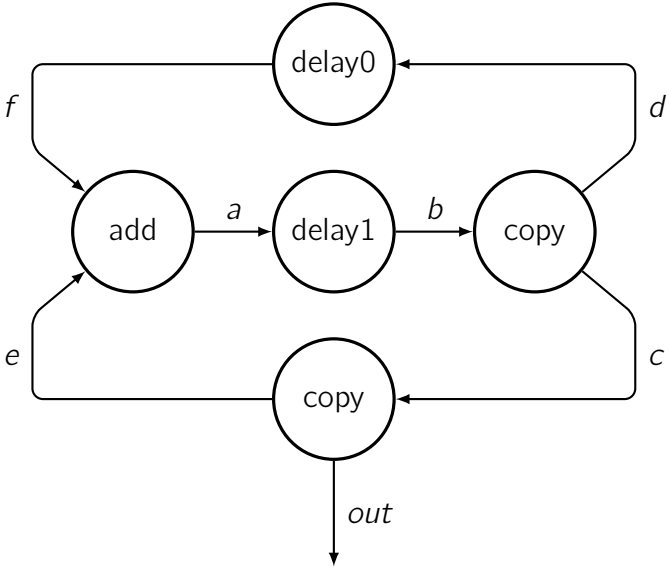
Luca Padovani

(with contributions from Tzu-Chun Chen, Luca Novara, Andrea Tosatto)

Dipartimento di Informatica – Università di Torino

Demo

Demo



Stages

- ① linearity analysis
⇒ partition channels into **linear** and **non-linear**
- ② protocol analysis (optional)
⇒ infer **communication structure**
- ③ deadlock analysis
⇒ no pending communications in **stable** states
- ④ lock analysis
⇒ pending communications in **all** states can be completed

Why the focus on linear channels?

- Kahn process networks

[Kahn '74]

- $\sim 50\%$ channels are linear

[Kobayashi, Pierce, Turner '99]

- binary sessions

[Kobayashi '07, Demangeon and Honda '11, Dardha et al. '12]

- multiparty sessions

[Padovani '13, Pérez et al. '14]

Outline

- ① Introduction
- ② Linearity analysis
- ③ Protocol analysis
- ④ Deadlock analysis
- ⑤ Lock analysis
- ⑥ Final remarks

Linearity analysis: how it works

 $\kappa_1, \kappa_2[t]$ `new a in { a!3 | a?x }`

- $\alpha_0 = \alpha_1 + \alpha_2$
- $\rho, \rho[\text{int}] = {}^{0,1}[\text{int}] + {}^{1,0}[\text{int}]$
- $\rho = 0 + 1$
- $\rho = 1 + 0$
- $\alpha_0 = {}^{1,1}[\text{int}]$

type combination \neq unification

Linearity analysis: how it works

 $\kappa_1, \kappa_2 [t]$ $\alpha_1 = {}^{0,1}[\text{int}]$ `new a in { a!3 | a?x }` $\alpha_0 = {}^{\rho,\rho}[\text{int}]$ $\alpha_2 = {}^{1,0}[\text{int}]$

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$$\alpha_1 = {}^{0,1}[\text{int}]$$

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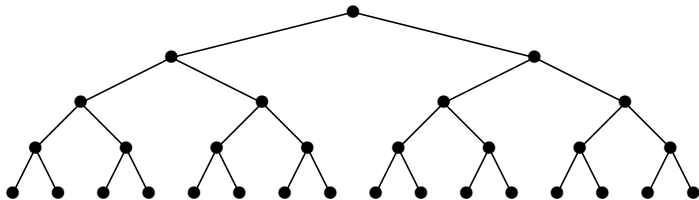
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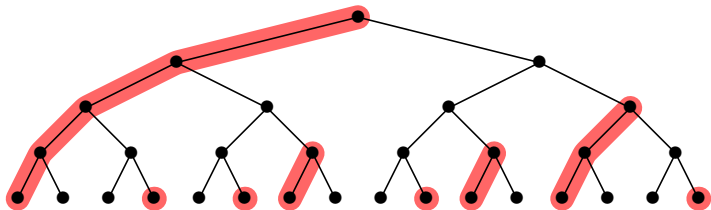
Demo: trees

```
*case take? of
  { Leaf          ⇒ {}
  ; Node(c,l,r) ⇒ c!0 | take!l | skip!r }
|
*case skip? of
  { Leaf          ⇒ {}
  ; Node(_,l,r) ⇒ skip!l | take!r }
|
take!t | skip!t
```

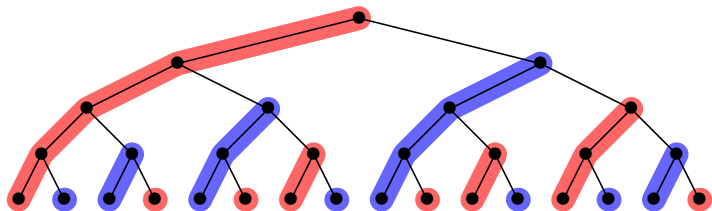
Channels used by take (red) and skip (blue)



Channels used by take (red) and skip (blue)



Channels used by take (red) and skip (blue)



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$s?(x).s!(x + 1)$

binary
sessions

linear
 π -calculus

session
types

linear
channel
types

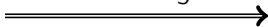
?int.!int...

$s?(x).s!(x + 1)$

$s?(x, s').\text{new } s'' \text{ in } s'!(x + 1, s'')$

binary
sessions

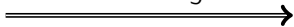
encoding



linear
 π -calculus

session
types

encoding



linear
channel
types

$?int.!int \dots$

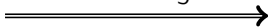
$^{1,0}[int \times ^{0,1}[int \times \dots]]$

$s?(x).s!(x + 1)$

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binary
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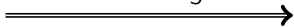
linear
 π -calculus

reconstruction



session
types

encoding



linear
channel
types

$?\text{int}.\text{!int} \dots$

$^{1,0}[\text{int} \times ^{0,1}[\text{int} \times \dots]]$

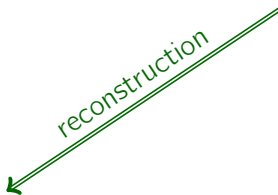
$s?(x).s!(x + 1)$

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binary
sessions

encoding

linear
 π -calculus



reconstruction



session
types

encoding
decoding

linear
channel
types



$?\text{int}.\text{!int} \dots$

$^{1,0}[\text{int} \times ^{0,1}[\text{int} \times \dots]]$

Demo: math server

```
*server?s.  
  case s? of  
  { Quit    ⇒ {}  
  ; Plus c1 ⇒ c1?(x,c2).  
              c2?(y,c3).  
              new c4 in { c3!(x + y, c4) | server!c4 }  
  ; Eq c1   ⇒ c1?(x:Int,c2).  
              c2?(y,c3).  
              new c4 in { c3!(x = y, c4) | server!c4 }  
  ; Neg c1  ⇒ c1?(x,c2).  
              new c3 in { c2!(0 - x, c3) | server!c3 } }
```

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Different processes, same typing

$a?x.b!x \mid a!3.b?y$

Different processes, same typing

$$a : {}^{1,1}[\text{int}], b : {}^{1,1}[\text{int}] \vdash a?x.b!x \mid a!3.b?y$$

Different processes, same typing

$a : {}^{1,1}[\text{int}], b : {}^{1,1}[\text{int}] \vdash a?x.b!x \mid a!3.b?y$

$a?x.b!x \mid b?y.a!3$



Different processes, same typing

$a : {}^{1,1}[\text{int}], b : {}^{1,1}[\text{int}] \vdash a?x.b!x \mid a!3.b?y$

$a : {}^{1,1}[\text{int}], b : {}^{1,1}[\text{int}] \vdash a?x.b!x \mid b?y.a!3$

Basic strategy for deadlock analysis

- 1 assign each linear channel a level $\in \mathbb{Z}$

$$\kappa_1, \kappa_2 [t]^h$$

- 2 make sure that channels are used in strict order

$$a ?x.b !x \mid b ?y.a !3$$

Basic strategy for deadlock analysis

- 1 assign each linear channel a level $\in \mathbb{Z}$

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$$a^m ? x . b^n ! x \mid b^n ? y . a^m ! 3$$

Basic strategy for deadlock analysis

- 1 assign each linear channel a level $\in \mathbb{Z}$

$$\kappa_1, \kappa_2 [t]^h$$

- 2 make sure that channels are used in strict order

$$a^m \overset{<}{\curvearrowright} x.b^n !x \mid b^n \overset{<}{\curvearrowright} y.a^m !3$$

A problem with recursive processes

```
*link?(x ,y ).  
  x ?(z,a ).           -- x blocks y and a  
  new b in y !(z,b ). -- y blocks a and b  
  link!(a ,b )
```

A problem with recursive processes

```
*link?(x0,y1).  
  x0?(z,a ).           -- x blocks y and a  
  new b in y1!(z,b ).  -- y blocks a and b  
  link!(a ,b )
```

A problem with recursive processes

```
*link?(x0,y1).  
  x0?(z,a2).           -- x blocks y and a  
  new b in y1!(z,b ).   -- y blocks a and b  
  link!(a2,b )
```


A problem with recursive processes

```
*link?(x0,y1).  
  x0?(z,a2).  
  new b3 in y1!(z,b3).  
  link!(a2,b3)
```

-- x blocks y and a
-- y blocks a and b

A problem with recursive processes

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Problem

- the levels of a and b don't match those of x and y
- type error

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Problem

- the levels of a and b don't match those of x and y
- type error

Solution

- + the mismatch is OK as long as it is a translation
- + allow level polymorphism

Type reconstruction: how it works

```
*link?(x ,y ).  
x ?(z,a ).      --  
new b in y !(z,b ).  --  
link!(a ,b )      --
```

- ① perform linearity analysis
- ② put integer variables in place of (unknown) levels
- ③ compute constraints
- ④ use ILP solver

Type reconstruction: how it works

```
*link?(xn, ym).  
xn?(z, ah).      --  
new bk in ym!(z, bk).  --  
link!(ah, bk)      --
```

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Type reconstruction: how it works

```
*link?(xn, ym).  
xn?(z, ah).           -- n < m ∧ n < h  
new bk in ym!(z, bk). --  
link!(ah, bk)         --
```

- ① perform linearity analysis
- ② put integer variables in place of (unknown) levels
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Type reconstruction: how it works

*link?(x^n, y^m).

$x^n?(z, a^h)$.

new b^k in $y^m!(z, b^k)$.

link!(a^h, b^k)

-- $n < m \wedge n < h$

-- $m < h \wedge m < k$

--

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*link?(x^n, y^m).

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-- $n < m \wedge n < h$

-- $m < h \wedge m < k$

-- $h = n + t \wedge k = m + t$

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Deadlocks vs locks

$0,1[\text{int}]^n$

`new a in { a!3 | c!a | *c?x.c!x }`

$1,1[\text{int}]^n$ $1,0[\text{int}]^n$

Strategy for lock analysis

- ① assign each linear channel a finite number $k \in \mathbb{N}$ of tickets

$$\kappa_1, \kappa_2 \left[t \right]_k^h$$

- ② each time a channel travels, one ticket is consumed
- ③ channels with no tickets cannot travel

Lock analysis and type reconstruction

```
*link?(x0, y1).  
x0?(z, a2).  
new b3 in y1!(z, b3).  
link!(a2, b3)
```

- ① perform linearity analysis
- ② put **natural** variables in place of (unknown) levels and tickets
- ③ compute constraints
- ④ use ILP solver

Lock analysis and type reconstruction

```
*link?(x00, y01).  
x00?(z, a2).  
new b3 in y01!(z, b3).  
link!(a2, b3)
```

- ① perform linearity analysis
- ② put **natural** variables in place of (unknown) levels and tickets
- ③ compute constraints
- ④ use ILP solver

Lock analysis and type reconstruction

```
*link?(x00, y01).  
x00?(z, a12).  
new b3 in y01!(z, b3).  
link!(a12, b3)
```

- ① perform linearity analysis
- ② put **natural** variables in place of (unknown) levels and tickets
- ③ compute constraints
- ④ use ILP solver

Lock analysis and type reconstruction

```
*link?(x00, y01).  
x00?(z, a12).  
new b23 in y01!(z, b13).  
link!(a12, b13)
```

- ① perform linearity analysis
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Related work

No levels

- sessions
- session types as linear logic propositions

(without interleaving)






Discrete levels

- Kobayashi [2002, 2006, ...]
- Bettini et al. [2008]
- Padovani, Vasconcelos, Vieira [2014]
- ...

Dense levels

- Giachino, Kobayashi, Laneve [2014]

Summary of products

-  Padovani, **Deadlock and Lock Freedom in the Linear π -Calculus** (LICS'14)
-  Padovani, **Type Reconstruction for the Linear π -Calculus with Composite and Equi-Recursive Types** (FoSSaCS'14)
-  Padovani, Chen, Tosatto, **Type Reconstruction Algorithms for Deadlock-Free and Lock-Free Linear π -Calculi** (submitted)
-  Padovani and Novara, **Types and Effects for Deadlock-Free Higher-Order Programs** (submitted)
-  Padovani and Tosatto, **Hypha**
(available at <http://di.unito.it/hypha>)